

09/921614

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,798,259 B2
DATED : September 28, 2004
INVENTOR(S) : Feng Lin

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4,

Line 4, insert -- I/O -- before "system".

Column 6,

Line 38, delete "PD" and replace it with -- phase detector --.

Column 7,

Line 40, replace "din" with -- d_{in} --.

Column 8,

Line 21, insert -- and -- before "adding".

Lines 39 and 48, replace "parameter t_c" with -- parameter t_e --.

Column 9,

Line 28, replace "arising" with -- a rising --.

Line 63, after "t_{mdl} = t_{ck};" insert -- and --.

Column 10,

Line 60, replace "CDLY signal" with -- CDLY signals --.

Line 65, replaec "tck" with -- t_{ck} --.

Line 67, replace "tmdl" with -- t_{mdl} --.

Column 11,

Lines 2-4, delete each occurrence of "wherein".

Line 19, replace "clod" with -- clock --.

Line 23, replace "characteristic" with -- characteristics --.

Line 30, replace "CIN as t_{mdl}" with -- CIN as t_{ck} --.

Line 46, replace "invert" with -- inverted --.

Line 48, replace "character" with -- characteristics --.

Line 61, replace "CDLY signal" with -- CDLY signals --.

Line 64, replace "block" with -- clock --.

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Column 12,

Line 17, replace "based an" with -- based on --.

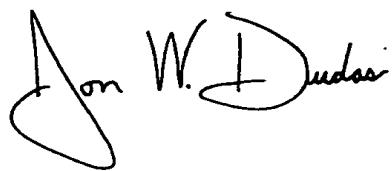
Line 34, replace "induced" with -- reduced --.

Line 54, replace "clack" with -- clock --.

Line 59, replace "timing characteristic" with -- timing characteristics --.

Signed and Sealed this

Twenty-fifth Day of January, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office